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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		1	Chang et al., "Using Profile Information to Assist Classic Code Optimizations", Software-Practice and Experience", Vol. 21(12), pp. 1301-21 (December 1991).
		2	Altman et al., "Welcome to the Opportunities of Binary Translation", pp. 40-53, IEEE Computer, March 2000.
		3	Altman et al., "Dynamic and Transparent Binary Translation", pp. 54-59, IEEE Computer, March 2000.
		4	Michael D. Smith, "Tracing with *pixie*", Computer Systems Laboratory at Standord University, report CSL-TR-91-497, pp. 1-29, November 1991.
		5	Lee et al., "An Efficient Software-Hardware Collaborative Profiling Technique for Wide-Issue Processors", Proceedings of the 1999 Workshop on Binary Translation, Newport Beach, CA., October 18, 1999, IEEE Computer Society Technical Committee on Computer Architecture Newsletter, pp. 34-42, December 1999.
		6	Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling", Proceedings of the 1994 Conference on Measurement and Modeling of Computer Systems, Nashville, TN., SIGMETRICS, pp. 128-37, May 1994.
		7	Altman et al., "Execution-based Scheduling for VLIW Architectures", EuroPar `99 Parallel Processing -- 5th International Euro-Par Conference, Berlin, Germany, pub. Springer Verlag, pp. 1269-80, August 1999.

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uy		8	Chrysos et al., "ProfileMe: Hardware Support for Instruction-Level Profiling on Out-of-Order Processors", Proceedings of the 30th Symposium on Microarchitecture (Micro-30), pp. 292-301, Dec. 1997.
		9	Altman et al., "An Eight-Issue Tree-VLIW Processor for Dynamic Binary Translation", Proceedings of the 1998 International Conference on Computer Design (ICCD `98) -- VLSI in Computers and Processors, Austin, TX., pub.
			IEEE Computer Society, pp. 488-95, Oct. 1998.
		10	Ebcioglu et al., "An Architectural Framework for Supporting Heterogeneous Instruction-Set Architectures" IEEE Computer, June 1993, pp. 39-56.
		11	Altman et al., Daisy: Dynamic Compilation for 100% Architectural Compatibility", Proceedings of the 24th Annual International Symposium on Computer Architecture (ISCA 97), Denver, CO., pub. ACM, pp. 26-37, June 1997.

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